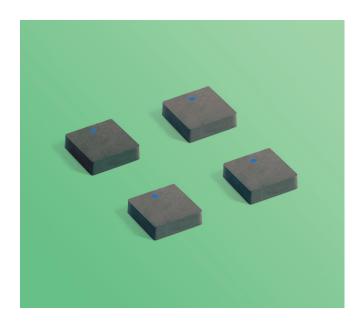
Outgassing Compliant Power Inductors AE483PYA



- Exceptionally low DCR; soft saturation
- Passes vibration testing to 80 G and shock testing to 1000 G
- Passes NASA low outgassing specifications
- High temperature materials allow operation in ambient temperatures up to 155°C.
- Tin-lead (Sn-Pb) terminations for the best possible board

Terminations Tin-lead (63/37) over copper.

Core material Composite

Weight 0.36 - 0.49 g

Ambient temperature -55°C to +105°C with Irms current Maximum part temperature +155°C (ambient + temp rise). Storage temperature Component: -55°C to +155°C.

Tape and reel packaging: -55°C to +80°C

Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)

Enhanced crush-resistant packaging 500 per 7"reel Plastic tape: 12 mm wide, 0.3 mm thick, 8 mm pocket spacing, 2.16 mm pocket depth

Part number ¹	Inductance ² ±20% (µH)	DCR (mOhms)3		SRF (MHz) ⁴			Irms (A)6	
		typ	max	min	typ	Isat (A) ⁵	20°C rise	40°C rise
AE483PYA121MSZ	0.12	1.60	1.85	132	165	45	15.8	20.3
AE483PYA161MSZ	0.16	2.35	2.70	121	152	41	15.0	19.5
AE483PYA271MSZ	0.27	3.45	3.85	77	97	29.5	14.3	18.8
AE483PYA451MSZ	0.45	4.60	5.05	58	73	24.5	12.8	16.5
AE483PYA601MSZ	0.60	6.45	7.10	52	66	20.5	11.3	13.9
AE483PYA901MSZ	0.90	10.63	11.10	44	56	19.1	8.6	11.4
AE483PYA112MSZ	1.1	12.60	13.10	40	50	17.1	7.5	9.0

1. When ordering, please specify **screening** code:

AE483PYA112MSZ

- **Screening: Z** = Unscreened
 - Y = Unscreened (SLDC Option A)
 - W = Unscreened (SLDC Option B)
 - H = Group A screening per Coilcraft CP-SA-10001
 - G = Coilcraft CP-SA-10001 Group A (SLDC Option A)
 - D = Coilcraft CP-SA-10001 Group A (SLDC Option B)
 - All screening performed to the document's latest revision
 - Custom screening also available
- 2. Inductance tested at 100 kHz, 0.1 Vrms, 0 Adc.
- 3. DCR measured on a micro-ohmmeter.
- 4. SRF measured using an Agilent/HP 4395A or equivalent.
- 5. DC current at 25°C that causes a 30% (typ) inductance drop from its value without current.
- 6. Current that causes the specified temperature rise from 25°C ambient. This information is for reference only and does not represent absolute maximum ratings.
- 7. Electrical specifications at 25°C.

Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

Irms Testing

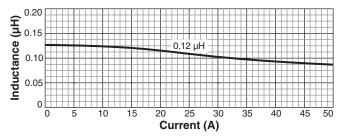
Irms testing was performed on a 0.060" thick pcb with 4 oz. copper traces optimized to minimize additional temperature rise.

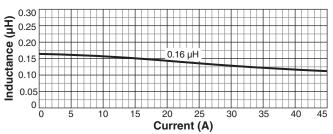
Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.

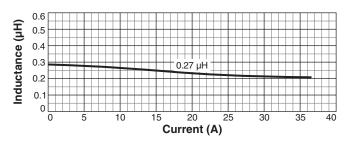


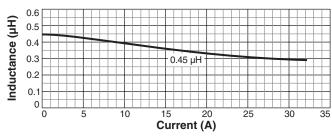
AE483PYA Series

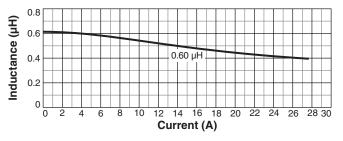
L vs Current

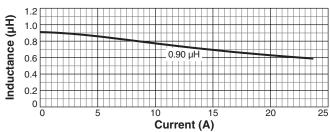


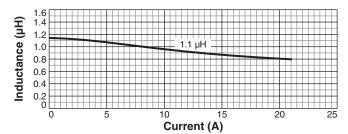






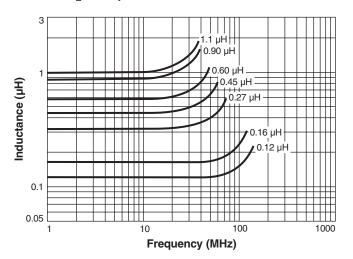


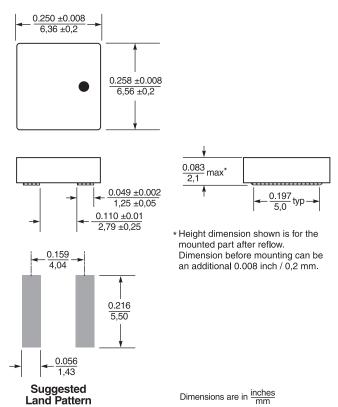




AE483PYA Series

L vs Frequency





Document AE801-3 Revised 04/12/23